

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4	(interface with controller with usb) and (nishino or wakabayashi).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:36
L2	4	(interface with controller with usb) and (nishino or wakabayashi or ichien).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:41
L3	2	(interface with controller with usb).clm. and (nishino or wakabayashi or ichien).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:36
L4	1	(interface with controller with usb).ab. and (nishino or wakabayashi or ichien).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:36
L5	0	(interface near controller near usb) and @ad<"20020924" and (nishino or wakabayashi or ichien).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:37
L6	3	(interface near controller near usb) and (nishino or wakabayashi or ichien).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:37
L7	33	(data same process\$5 same cpu same ram same (transfer\$5 with control\$6)) and (nishino or wakabayashi or ichien).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:37
L8	112	(interface with controller with usb) and (710/22.ccls. or 710/23.ccls. or 710/33.ccls. or 710/36.ccls. or 710/52.ccls. or 709/212.ccls. or 711/100.ccls. or 326/56.ccls. or 709/234.ccls. or 711/118.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:41
L9	12	(interface with controller with usb).clm. and (710/22.ccls. or 710/23.ccls. or 710/33.ccls. or 710/36.ccls. or 710/52.ccls. or 709/212.ccls. or 711/100.ccls. or 326/56.ccls. or 709/234.ccls. or 711/118.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:41

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L10	6	(data same process\$5 same cpu same ram same (transfer\$5 with control\$6) same fifo) and @ad<"20020924" and (710/22.ccls. or 710/23.ccls. or 710/33.ccls. or 710/36.ccls. or 710/52.ccls. or 709/212.ccls. or 711/100.ccls. or 326/56.ccls. or 709/234.ccls. or 711/118.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:41
L11	5	(interface near controller near usb) and @ad<"20020924" and (710/22.ccls. or 710/23.ccls. or 710/33.ccls. or 710/36.ccls. or 710/52.ccls. or 709/212.ccls. or 711/100.ccls. or 326/56.ccls. or 709/234.ccls. or 711/118.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:41
L12	1	((data with processor) same cpu same (interface with controller) same (bus with controller) same fifo same (transfer\$6 with control) same (address or semiconductor or chip)) and (710/22.ccls. or 710/23.ccls. or 710/33.ccls. or 710/36.ccls. or 710/52.ccls. or 709/212.ccls. or 711/100.ccls. or 326/56.ccls. or 709/234.ccls. or 711/118.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:41
L13	1	(interface with controller with usb) and (nishino or wakabayashi or ichien).in. and (710/22.ccls. or 710/23.ccls. or 710/33.ccls. or 710/36.ccls. or 710/52.ccls. or 709/212.ccls. or 711/100.ccls. or 326/56.ccls. or 709/234.ccls. or 711/118.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:42
S1	9	((data with processor) same cpu same (interface with controller) same (bus with controller) same fifo same (transfer\$6 with control) same (address or semiconductor or chip))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:41
S3	6	((data with processor) same cpu same (interface with controller) same (bus with controller) same fifo same (transfer\$6 with control) same (address or semiconductor or chip)) and @ad<"20020924"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 20:08

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S4	10	((data with process\$4) same cpu same (interface with controller) same (bus with controller) same fifo same (transfer\$6 with control) same (address or semiconductor or chip)) and @ad<"20020924"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 19:40
S11	3729	(interface with controller with usb)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:40
S12	122	(interface near controller near usb)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 13:00
S13	62	(interface near controller near usb) and @ad<"20020924"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:41
S14	0	((interface near controller near usb) same fifo same cpu) and @ad<"20020924"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 13:01
S15	2	((interface near controller near usb) same fifo) and @ad<"20020924"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 13:01
S16	0	((interface near controller near usb) same fifo same processor) and @ad<"20020924"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 13:01
S17	0	((interface near controller near usb) same fifo same process\$4) and @ad<"20020924"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 13:01
S18	0	((interface near controller near usb) same fifo same apparatus) and @ad<"20020924"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 13:01

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S19	2	((interface near controller near usb) same fifo same device) and @ad<"20020924"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 13:01
S20	2	((interface near controller near usb) same fifo same transfer same control\$4) and @ad<"20020924"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 13:26
S22	2394	(data same process\$5 same cpu same ram same (transfer\$5 with control\$6)) and @ad<"20020924"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:37
S23	96	(data same process\$5 same cpu same ram same (transfer\$5 with control\$6) same fifo) and @ad<"20020924"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 15:41
S24	1	(data same process\$5 same cpu same ram same (transfer\$5 with control\$6) same fifo same (read same write same cycle)) and @ad<"20020924"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 13:29